

IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Yih-Feng Chyan
John Michael Hergenrother
Donald Paul Monroe

CASE 15-6-9

TITLE Architecture For Circuit Connection Of A Vertical Transistor

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

NEW APPLICATION UNDER 37 CFR § 1.53(b)

Enclosed are the following papers relating to the above-named application for patent:

Specification
7 Informal Sheets of drawing(s)

CLAIMS AS FILED				
	NO. FILED	NO. EXTRA	RATE	CALCULATIONS
Total Claims	31 - 20 =	11	x \$18 =	\$198
Independent Claims	5 - 3 =	2	x \$78 =	\$156
Multiple Dependent Claims, if applicable			+ \$260 =	\$0
Basic Fee				\$690
			TOTAL FEE	\$1044

Please file the application and charge **Lucent Technologies Deposit Account No. 12-2325** the amount of \$1044, to cover the filing fee. Duplicate copies of this letter are enclosed. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Deposit Account No. 12-2325** as required to correct the error.

The Assistant Commissioner for Patents is hereby authorized to treat any concurrent or future reply, requiring a petition for extension of time under 37 CFR § 1.136 for its timely submission, as incorporating a petition for extension of time for the appropriate length of time if not submitted with the reply.

Please address all correspondence to **Docket Administrator (Room 3C-512), Lucent Technologies Inc., 600 Mountain Avenue, P.O. Box 636, Murray Hill, New Jersey 07974-0636**. However, telephone calls should be made to me at 407-371-3250.

Respectfully,

Scott W. McLellan
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Date: August 25, 2000
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number EJ 321954332 US
Date of Deposit Aug. 25, 2000
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